

J/K
PPL 18.01
N. Little

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: Unassigned
Examiner: Unassigned

In Re PATENT APPLICATION Of:

Applicants	:	Jen-Kai CHEN et al.)
Appln. No.	:	Unassigned)
Filed	:	May 16, 2001)
For	:	METHOD FOR COMPENSATING DIFFERENT CLOCK SIGNALS BETWEEN SWITCH AND PERIPHERAL DEVICE)
Attorney Ref.	:	SUND 202)

**PRELIMINARY
AMENDMENT**

Commissioner for Patents
Washington, D.C. 20231

Sir:

Preliminary to examination, please amend the application as follows:

IN THE CLAIMS

Please amend claims 14 and 20, and add new claims 21 and 22 as follows:

14. (Amended) The method of claim 13, wherein the N-th queue link node QLN(N) further comprises a fifth field for recording a source port speed of the N-th packet.

20. (Amended) The method of claim 19, wherein the N-th link node QLN(N) further comprises a fifth field for recording a source port speed of the N-th packet.

--21. The method of claim 18, wherein the N-th link node QLN(N) further comprises a fifth field for recording a source port speed of the N-th packet.

22. The method of claim 12, wherein the N-th link node QLN(N) further comprises a fifth field for recording a source port speed of the N-th packet.--

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QZ